

ABSTRACT OF THE DISCLOSURE

The present invention relates to a device for performing predetermined processing on a signal inputted thereto that may have signal amplitude of more than one bit, which signal is obtained by subjecting one-bit serial signals to predetermined signal processing, wherein the signal amplitude of more than one bit is converted to a one-bit serial signal by accumulating the signal amplitude exceeding that of one bit, delaying the accumulated signal on the basis of the input signal, and outputting the accumulated signal.